

Dr. D Y Patil Pratishthan's

# D. Y. Patil College of Engineering, Akurdi, Pune

Accredited by NAAC with A Grade

Electronics & Telecommunication Department

Organizes Online

## Faculty Development Program

On

# *“Modeling & Simulation tools for Research”*

30<sup>th</sup> August (Monday) to 3<sup>rd</sup> September (Friday), 2021

## About DYPCOE

D. Y. Patil College of Engineering, Akurdi was established in 1984, approved by AICTE, New Delhi and is affiliated to SPPU. The College is presently spread over 35 acres of land at Akurdi, which is in the vicinity of Pimpri–Chinchwad Industrial belt, one of the biggest Industrial belts in Asia. The college has eight UG Programs, seven PG Programs & One Research Center approved by SPPU. The perfect blend of infrastructure and Human resources, with rich experience and competent qualification are able to achieve the set goals and comply with the Vision and Mission of the Institute.

## About the Department

The Department of E&TC Engineering was established in the year 2001 with intake of 120. Department also has approved PG course in VLSI & Embedded systems & Ph.D Research Center affiliated to Savitribai Phule Pune University (SPPU) & 19 research scholars are pursuing their Ph.D. Students are placed in high reputed industries, few opted higher studies in renowned institutions and few initiated startups to make department and college feel proud.

## About the Program

Tremendous innovation in Electronics and Telecommunication had a considerable impact on research. Its diverse advancement has resulted in a variety of technologies that were previously unthinkable. The goal of this Faculty Development Program is to provide researcher with a knowledge of the various platforms used in mathematical modeling and simulation to report results. Modeling is used to build a mathematical model of the physical System and conditions are applied that set up the experiment of interest. The simulation represents the real systems via mathematical models that allow representing the dynamics of the system & exploring system behavior in an articulated way.

## Resource Persons

- ✓ **Dr. Aditya Abhyankar**  
Dean, Faculty of Technology and Professor in. Department of Technology of SP Pune University
- ✓ **Dr. Manesh Babanrao Kokare**  
Associate Professor, Shri Guru Gobind Singhji (SGGS) Institute of Engineering & Technology, Nanded
- ✓ **Cdr Dr Rupak Berry**  
Adjunct Professor, Department of Technology, Pune University
- ✓ **Dr. Bhushan Patil**  
Lead Scientist at General Electric (Healthcare)
- ✓ **Mrs. Asawari Palshikar**  
Sr. Data Analyst, Persistent Systems
- ✓ **Mr. Nitin Banaik,**  
Manager - Design Engineering at Quadrogen India
- ✓ **Mr. Prathamesh Mane,**  
Analog Layout Designer, Qualcomm
- ✓ **Mr. Mahesh Shinde,**  
VLSI Engineer at Wipro Limited

## Registration Details

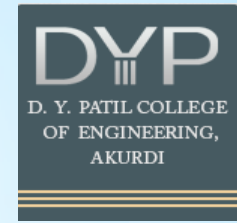
Registration Fees is of Rs. 100/- to be paid to

Punjab National bank, Akurdi, Pune.

Acc No – 21741132000531 IFSC : PUNB0217410

OR Google pay @ 8007043081

<https://forms.gle/WB6u6pb7UY35T7xF7>



**Chief Patron**

**Padmashree Hon. Dr. D. Y. Patil**  
Founder, Dr. D. Y. Patil Pratishthan

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**Patrons**

**Hon. Dr. Sanjay D. Patil**  
President, Dr. D. Y. Patil Pratishthan

**Hon. Shri. Satej D. Patil**  
Chairman  
Dr. D. Y. Patil Educational Complex, Akurdi

**Dr. Neeraj Vyawahare**  
Campus Director (I/C)

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**Organizing Chair**

**Dr. Vijay M. Wadhai**  
Principal, DYPCOE

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**Convener**

**Dr. Mrs. P. Malathi**  
H.O.D. E&TC

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**FDP Coordinators**  
**Dr. Manish Sharma**  
**Dr. Rutuja Deshmukh**

**For Registration contact:**  
**Mrs. S. N. Mane - 9422540740**  
**Mrs. V. M. Lakshe - 9665163654**